

Design and Analysis of security system in GSM Trans-receiver

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Abstract—A global system for mobile communication is digital cellular communication system called as GSM. In wireless communication technology, quality of voice output at destination depends on the channel condition. Bad channel condition will produce distortion in the voice output and hence degrades the voice quality. Low bit rate modes is used in a bad channel condition to allow more bits for channel coding, while high bit rate modes on the contrary. Various speech coding techniques, such as a LPC (linear predicting coding) and RPE-LTP (regular pulse excitation) are used in different application. The report work addresses the challenges and opportunities starting from the basic issues. In speech coding techniques and standards, discussing current and future application outlining techniques for evaluating speech coder performance, and identify research direction. The most prominent speech coding standards are presented and their properties, such as performance, complexity, and coding delay, analyzed.

Keywords—MATLAB & Simulink.

I. INTRODUCTION

The speech coding is a procedure to represent a digitized speech signal using as few bits as possible, maintaining at the same time a reasonable level of speech quality. Speech coding has matured to the point where it now constitutes an important application area of signal processing.

Figure 1.1 shows the block diagram of a speech coding system. The continuous time analog speech signal from a given source is digitized by a standard connection of filter using for eliminates aliasing, sampler which is use for discrete-time conversion, and analog-to digital converter using for uniform quantization is assumed. The output is a discrete time speech signal whose sample values are also discretized. This signal is referred to as the digital speech.

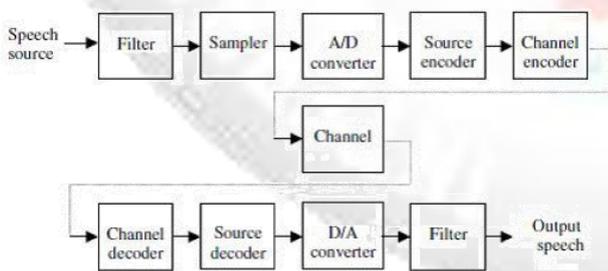


Figure 1.1: Block diagram of a speech coding system.

Traditionally, most speech coding systems were designed to support telecommunication applications, with the frequency contents limited between 300 and 3400 Hz. According to the Nyquist theorem [10], the sampling frequency must be at least twice the bandwidth of the continuous-time signal in order to avoid aliasing. A value of 8kHz is commonly selected as the standard sampling frequency for speech

signals. To convert the analog samples to a digital format using uniform quantization, the digital speech will be roughly indistinguishable from the band-limited input more than 8 bits/sample is necessary. The use of 13 bits/sample provides a quality that is considered high. The following parameters are assumed for the digital speech signal:

- Sampling frequency = 8 kHz
- Number of bits per sample = 13
- This gives rise to
- Bit-rate = 8 kHz * 13 bits = 104 kbps

The above bit-rate, also known as input bit-rate, is what the source encoder attempts to reduce. Figure 1.1. The output of the source encoder represents the encoded digital speech and in general has substantially lower bit-rate than the input. The linear prediction coding algorithm [17], for instance, has an output rate of 2.4 kbps, a reduction of more than 53 times with respect to the input. The encoded digital speech data is further processed by the channel encoder, providing error protection to the bit-stream before transmission to the communication channel, where various noise and interference can sabotage the reliability of the transmitted data. Even though in Figure 1.1 the source encoder and channel encoder are separated, it is also possible to jointly implement them so that source and channel encoding are done in a single step.

The channel decoder processes the error-protected data to recover the encoded data, which is then passed to the source decoder to generate the output digital speech signal, having the original rate. This output digital speech signal is converted to continuous time analog form through standard procedures: digital-to-analog conversion followed by anti-aliasing filtering [10].

II. PRINCIPLE DESIGN

• Traffic Channel for GSM System

The sequence of operation of a GSM sender and receiver is depicted in Figure 2.1

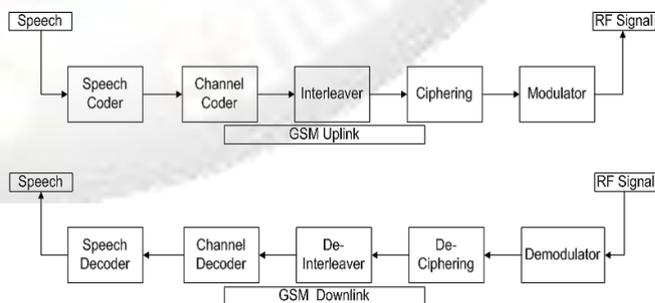


Figure 2.1: Block diagram of a GSM sender and receiver

BTS
The signal created by a microphone is an analog signal. Since GSM is an all digital system, this analog signal is not suitable for use on a GSM network. The analog signal must be converted into digital form. This is done by using an

Analog to Digital Converter (ADC). In order to reduce the amount of data needed to represent the sound wave, the analog signal is first inputted into a band pass filter.

A. Speech Coding and Decoding:

1) Analog to Digital Conversion:

In order to reduce the amount of data needed to represent the sound wave, the analog signal is first inputted into a band pass filter. The Band Pass Filter only allows frequencies between 300 Hz and 3.4 kHz to pass through it as shown in Figure. This limits the amount of data that the Analog Digital Converter is required to process. The filtered signal is inputted into the analog to digital converter. The analog to digital converter performs two tasks. It converts an analog signal into a digital signal and it does the opposite, converts a digital signal into an analog signal.



The A/D converter measures the analog signal, or samples it 8000 times per second. This means that the ADC takes a sample of the analog signal every .125 sec. Each sample is quantified with a 13-bit data block. If we calculate 13 bits per sample at 8000, we determine a data rate of 104,000 bits per second, or 104 Kb/s as shown in samples per-second, we determine a data rate of 104,000 bits per second, or 104 Kb/s as shown in figure 2.2

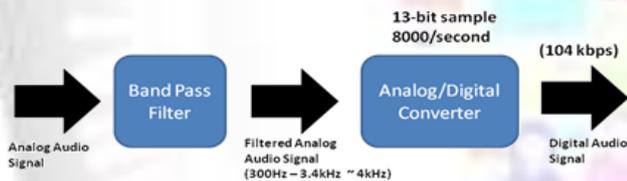


Figure. 2. 2 :ADC: Quantization Process

• Speech Encoder :

The speech encoder used in GSM is called Linear Predictive Coding (LPC) and Regular Pulse Excitation (RPE). Speech coder maps speech into digital blocks. Coder used in GSM phase 1 compresses the speech signal to 13 kbps rate using the Rectangular Pulse Excited Linear Predictable coding with Long Term Prediction (RPE-LTP) technique as per GSM 06-10 specification. And in the GSM phase 2 half rate (HR) Enhance Full Rate (EFR), scheme achieves a rate of 5.6 kbps. EFR provides same quality and better performance than RPE-LTP. These algorithms produce a speech block of 260 bits every 20 ms as shown in Figure 2.3

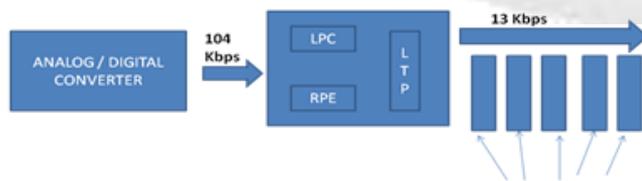


Figure 2.3: RPE – LTP speech coder

Description	Formula	Result
Convert ms to sec	20ms=1000	0.02 sec

Calculate bits per sec	260bits=0.02 sec	13 kbps
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B. Channel Coding and Decoding

Channel Coding is itself consist of two phase they are as follows.

1) Block Coding

A single 260-bit (20ms) audio block is delivered to the block-coder. The 260 bits are divided up into classes according to their importance in reconstructing the audio as shown in Figure 2.4.

Class Ia - 50 bits (most sensitive to bit errors)

Class Ib - 132 bits (moderately sensitive to bit errors)

Class II - 78 bits (least sensitive to error)

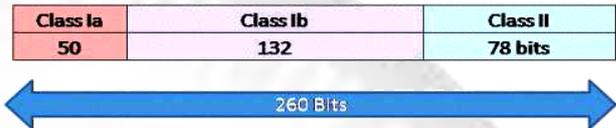
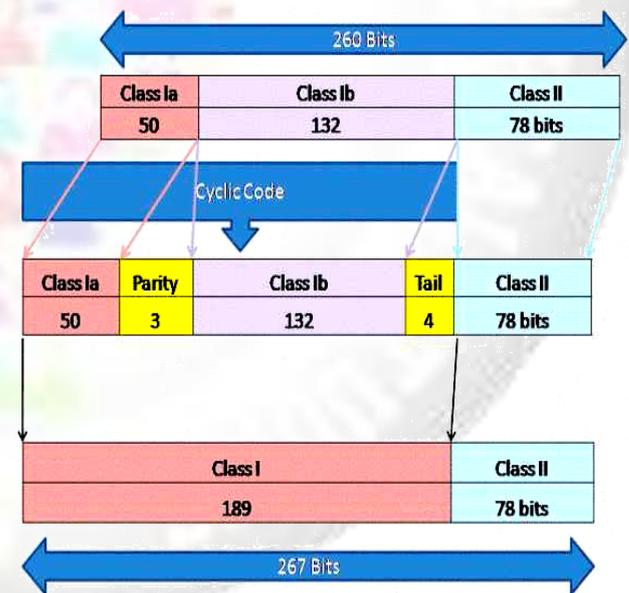


Figure. 2.4: classes of bits

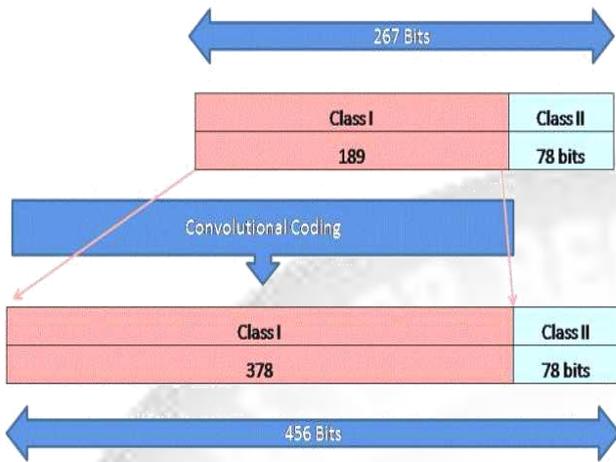
The Class Ia bits are protected by a Cyclic Code (CRC Coding). The cyclic code is run on the 50 Ia bits and calculates 3 parity bits which are then appended to the end of the Ia bits as shown in Figure. Only the class Ia bits are protected by this cyclic code. The Class Ia and Ib bits are then combined and an additional 4 bits are added to the tail of the class I bits (Ia and Ib together). All four bits are zeros (0000) and are needed for the next step which is "Convolutional Coding". There is no protection for Class II bits. As you can see, block coding adds seven bits to the audio block, 3 parity bits and 4 tail bits, therefore, a 260-bit block becomes a 267-bit block.



2) Convolution Coding :

This 189-bit block is then fed into a convolutional coder. Convolutional coding allows errors to be detected and to be corrected to a limited degree. The Convolution encoder is used for internal error correction. For every k input bits the Convolution encoder produces n output bits. The output bits depends not only on the current input bit but also on previous input bits. The number of previous input bits, which govern the output, is termed the Constraint length. This coding uses 5 consecutive bits to calculate the

redundancy bit, this is why there are 4 bits added to the Class I bits when the cyclic code was calculated. The number of class I bits is doubled from 189 to 378 bits as shown in Figure.



3) Reordering, Partitioning, and Interleaving

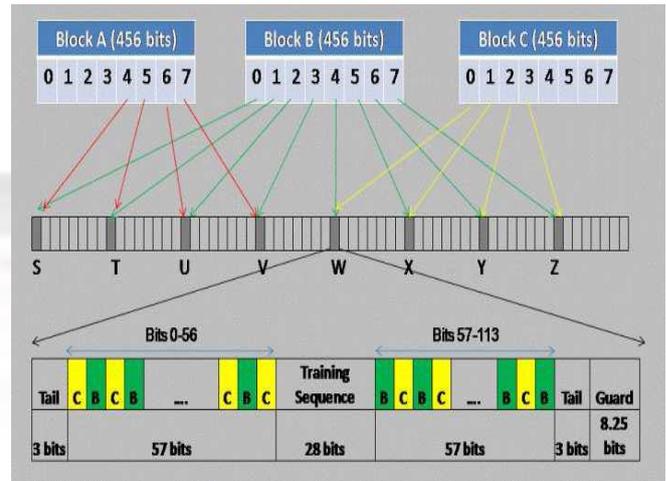
Now, one problem remains. All of this error detection and error correction coding will not do any good if the entire 456-bit block is lost or garbled. In order to alleviate this, the bits are reordered and partitioned into eight separate sub-blocks. If one sub-block is lost then only one-eighth of the data for each audio block is lost and those bits can be recovered using the convolutional code on the receiving end. This is known as *interleaving*. Each 456-bit block is reordered and partitioned into 8 sub-blocks of 57 bits each. to see the ordering sequence.

These eight 57-bit sub-blocks are then interleaved onto 8 separate bursts. As you remember from the TDMA Tutorial, each burst is composed of two 57-bit data blocks, for a total data payload of 114 bits.

The first four sub-blocks (0 through 3) are mapped onto the even bits of four consecutive bursts. The last four sub-blocks (4 through 7) are mapped onto the odd bits of the next 4 consecutive bursts. So, the entire block is spread out across 8 separate bursts. Taking a look at the diagram below we see three 456-bit blocks, labeled A, B, and C. Each block is subdivided into eight sub-blocks numbered 0-7. Let's take a look at Block B. We can see that each sub-block is mapped to a burst on a single time-slot. Block B is mapped onto 8 separate bursts or time-slots. For illustrative purposes, the time-slots are labeled S through Z. Let's expand time-slot V for a close-up view. We can see how the bits are mapped onto a burst. The bits from Block B, sub-block 3 (B3) are mapped onto the even numbered bits of the burst (bits 0, 2, 4, ..., 108, 110, 112). You will also notice that the odd bits are being mapped from data from block A, sub-block 7 (bits 1, 3, 5, ..., 109, 111, 113). Each burst contains 57 bits of data from two separate 456-bit blocks. This process is known as *interleaving*.

In the following diagram, we examine time-slot W. We see that bits from B4 are mapped onto the odd-number bits (bits 1, 3, 5, ..., 109, 111, 113) and we would see bits from C1 mapped onto the even number bits (bits 0, 2, 4, ..., 108, 110, 112). This process continues indefinitely as

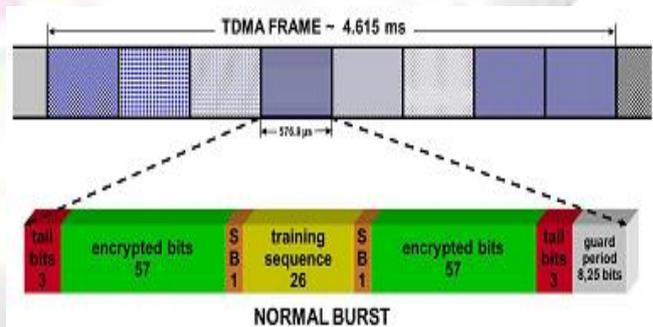
data is transmitted. Time-slots W, X, Y, and Z would all be mapped identically. The next time-slot would have data from Block C and Block D mapped onto it. This process continues for as long as there is data being generated.



4) Interleaving :

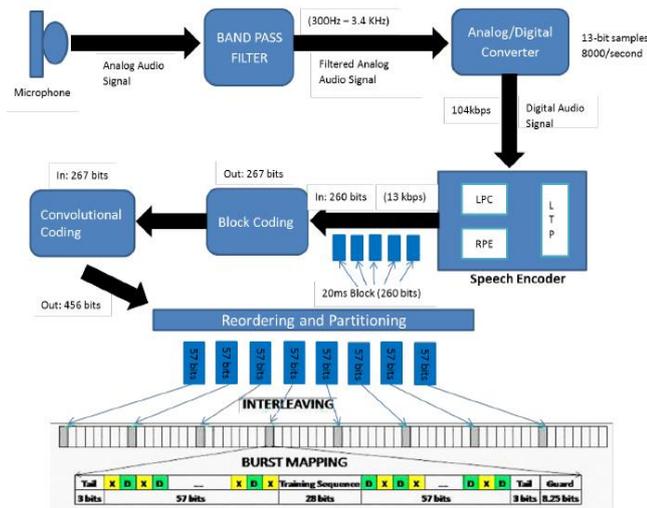
The process of interleaving effectively distributes a single 456 bit audio block over 8 separate bursts. If one burst is lost, only 1/8 of the data is lost, and the missing bits can be recovered using the convolutional code. Now, you might notice that the data it takes to represent a 20ms (456-bits) audio block is spread out across 8 time slots. If you remember that each TDMA frame is approximately 4.615ms, we can determine that it takes about 37ms to transmit one single 456-bit block. It seems like transmitting 20ms worth of audio over a period of 37ms would not work. However, this is not what is truly happening. If you look at a series of blocks as they are mapped onto time-slots you will notice that one sub-block ends every four time-slots, which is approximately 18ms. The only effect this has is that the audio stream is effectively delayed by 20ms, which is truly negligible. We also do for block c and block The following diagram illustrates the entire process, from audio sampling to partitioning and interleaving.

III. BURST MAPPING



Tail Bits- All-zero bits to indicate the start and the end of the burst. **Data Bits** :- Speech data is to carry after interleaving and Burst mapping. **Training Bits** :- For channel adaptive equalization (is an equalizer that automatically adapts to time-varying properties of the communication channel.) **Guard Bits**-ramping time for transmitter ON/OFF, to avoid overlapping between adjacent time slots. Necessarily much longer for Access Burst.

– Summary:



IV. CIPHERING & DE-CIPHERING :

For security in cellular telecommunications systems are to secure conversation we use these terms “CIPHERING & Deciphering”. Ciphering is used to change the data patterns. It does not depend on the type of data to be transmitted, but is only applied to normal bursts. This is achieved by performing an XOR operation between a pseudorandom bit sequences and the 114 bits of each bursts. Ciphering is used at transmitter side and de-ciphering is used at receiver side. For Ciphering method I have pass my bit stream from X-OR gate

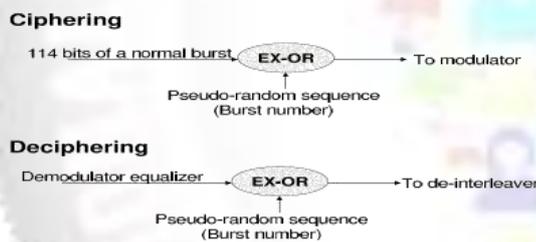


Figure GSM Ciphering / Deciphering

V. CONCLUSIONS

This is an ongoing project. In this paper, a comparative study of GSM transceiver is presented. In future work, we are going to implement uplink modules, which basically contain the GSM speech coder, channel encoder, interleaver, cipher, and modulator; similarly, downlink modules contain the demodulator or equalizer, decipher, deinterleaver, channel decoder, and speech decoder. This chapter is organized for GSM uplink and downlink, and their implementation in software using MATLAB Simulink also reduces the BER rate in GSM.

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