

HCC based Single-Stage PFC Half-Bridge Converter Driven PMBLDCM

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Abstract— In Real world PMBLDCM's are used to drive a compressor load of an air conditioner through a three-phase VSI fed from a controlled DC link voltage. The speed of the compressor is controlled to get energy conservation using voltage control at DC link proportional to the desired speed of the PMBLDCM. So, the VSI is operated only as an electronic commutator of the PMBLDCM. The stator current of the PMBLDCM during step change of reference speed is controlled by a rate limiter for the reference voltage at DC link. In this paper, a buck half-bridge DC-DC converter is used as a single-stage power factor correction (PFC) converter for feeding a voltage source inverter (VSI) based permanent magnet brushless DC motor (PMBLDCM) drive. The Control scheme utilizes the Hysteresis based controlling action. The proposed PMBLDCM drive with voltage control Based PFC converter is designed, modeled and its performance is simulated in Matlab/Simulink.

Keywords— PFC, PMBLDCM, Air conditioner, Buck Half bridge converter, Voltage control, VSI, HCC

I. INTRODUCTION

Permanent magnet brushless DC motors (PMBLDCMs) are preferred motors for a compressor of an air-conditioning (Air-Con) system due to its features like high efficiency, wide speed range and low maintenance requirements [1-4]. The operation of the compressor with the speed control results in an improved efficiency of the system while maintaining the temperature in the air-conditioned zone at the set reference consistently. Whereas, the existing air conditioners mostly have a single-phase induction motor to drive the compressor in 'on/off' control mode. This results in increased losses due to frequent 'on/off' operation with increased mechanical and electrical stresses on the motor, thereby poor efficiency and reduced life of the motor.

Moreover, the temperature of the air conditioned zone is regulated in a hysteresis band. Therefore, improved efficiency of the Air-Con system will certainly reduce the cost of living and energy demand to cope-up with ever-increasing power crisis.

A PMBLDCM which is a kind of three-phase synchronous motor with permanent magnets (PMs) on the rotor and trapezoidal back EMF waveform, operates on electronic commutation accomplished by solid state switches. It is powered through a three-phase voltage source inverter (VSI) which is fed from single-phase AC supply using a diode bridge rectifier (DBR) followed by smoothing DC link capacitor. The compressor exerts constant torque (i.e. rated torque) on the PMBLDCM and is operated in speed control mode to improve the efficiency of the Air-Con system. Since, the back-emf of the PMBLDCM is proportional to the motor speed and the developed torque is proportional to its phase current [1-4], therefore, a constant torque is maintained by a constant current in the

stator winding of the PMBLDCM whereas the speed can be controlled by varying the terminal voltage of the motor. Based on this logic, a speed control scheme is proposed in this paper which uses a reference voltage at DC link proportional to the desired speed of the PMBLDC motor. However, the control of VSI is only for electronic commutation which is based on the rotor position signals of the PMBLDC motor.

The PMBLDCM drive, fed from a single-phase AC mains through a diode bridge rectifier (DBR) followed by a DC link capacitor, suffers from power quality (PQ) disturbances such as poor power factor (PF), increased total harmonic distortion (THD) of current at input AC mains and its high crest factor (CF). It is mainly due to uncontrolled charging of the DC link capacitor which results in a pulsed current waveform having a peak value higher than the amplitude of the fundamental input current at AC mains. Moreover, the PQ standards for low power equipments such as IEC 61000-3-2 [5], emphasize on low harmonic contents and near unity power factor current to be drawn from AC mains by these motors. Therefore, use of a power factor correction (PFC) topology amongst various available topologies [6-14] is almost inevitable for a PMBLDCM drive. Most of the existing systems use a boost converter for PFC as the front-end converter and an isolated DC-DC converter to produce desired output voltage constituting a two-stage PFC drive [7-8]. The DC-DC converter used in the second stage is usually a flyback or forward converter for low power applications and a full-bridge converter for higher power applications. However, these two stage PFC converters have high cost and complexity in implementing two separate switch-mode converters, therefore a single stage converter combining the PFC and voltage regulation at DC link is more in demand. The single-stage PFC converters operate with only one controller to regulate the DC link voltage along with the power factor correction. The absence of a second controller has a greater impact on the performance of single-stage PFC converters and requires a design to operate over a much wider range of operating conditions. For the proposed voltage controlled drive, a half-bridge buck DC-DC converter is selected because of its high power handling capacity as compared to the single switch converters. Moreover, it has switching losses comparable to the single switch converters as only one switch is in operation at any instant of time. It can be operated as a single-stage power factor corrected (PFC) converter when connected between the VSI and the DBR fed from single-phase AC mains, besides controlling the voltage at DC link for the desired speed of the Air-Con compressor. A detailed modeling, design and performance evaluation of the proposed drive are presented for an air conditioner compressor driven by a PMBLDC motor of 1.5 kW, 1500 rpm rating.

II. PROPOSED SPEED CONTROL SCHEME OF PMBLDC MOTOR FOR AIR CONDITIONER

The proposed speed control scheme (as shown in Fig. 1) controls reference voltage at DC link as an equivalent reference speed, thereby replaces the conventional control of the motor speed and a stator current involving various sensors for voltage and current signals. Moreover, the rotor position signals are used to generate the switching sequence for the VSI as an electronic commutator of the PMBLDC motor. Therefore, rotor-position information is required only at the commutation points, e.g., every 60° electrical in the three-phase [1-4]. The rotor position of PMBLDCM is sensed using Hall effect position sensors and used to generate switching sequence for the VSI as shown in Table-I. The DC link voltage is controlled by a half-bridge buck DC-DC converter based on the duty ratio (D) of the converter. For a fast and effective control with reduced size of magnetic and filters, a high switching frequency is used; however, the switching frequency (fs) is limited by the switching device used, operating power level and switching losses of the device. Metal oxide field effect transistors (MOSFETs) are used as the switching device for high switching frequency in the proposed PFC converter. However, insulated gate bipolar transistors (IGBTs) are used in VSI bridge feeding PMBLDCM, to reduce the switching stress, as it operates at lower frequency compared to PFC switches. The PFC control scheme uses a current control loop inside the speed control loop with current multiplier approach which operates in continuous conduction mode (CCM) with average current control. The control loop begins with the comparison of sensed DC link voltage with a voltage equivalent to the reference speed. The resultant voltage error is passed through a proportional-integral (PI) controller to give the modulating current signal. This signal is multiplied with a unit template of input AC voltage and compared with DC current sensed after the DBR. The resultant current error is fed to Hysteresis based current controller to generate pulses for the half-bridge converter. For the current control of the PMBLDCM during step change of the reference voltage due to the change in the reference speed, a voltage gradient less than 800 V/s is introduced for the change of DC link voltage, which ensures the stator current of the PMBLDCM within the specified limits (i.e. double the rated current).

Hysteresis current control: Hysteresis current control is one of PWM methods used for generating pulses to order the power switches of inverter. Among the various current control techniques, HCC is widely used due to the fast response, simple implementation, negligible tracking error, inherent robustness to load parameters variations and proper stability. As it is mentioned in the research, HCC has high accuracy and fast response as well as provides better low-order harmonic suppression than PWM control which is the main target of the active power filter. However, as a disadvantage, its switching frequency might fluctuate which is the matter of importance in this study.

As shown in Fig.2, the difference between reference current and actual current is called error signal (e). the error signal is sent into the hysteresis bands, if it touches the upper or lower band, the hysteresis controller block decides to generate associated switching pulses to keep the

error signal in desired area. The outputs of the hysteresis blocks are directly fed as the firing pulse of VSI switches.

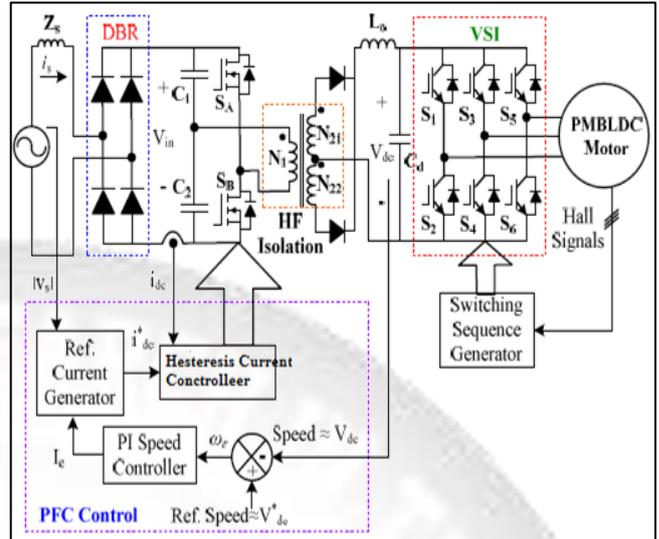


Fig. 1: Control schematic of Proposed Bridge-buck PFC converter fed PMBLDCM drive

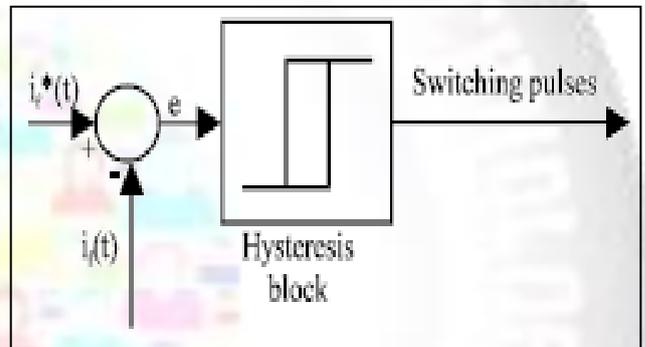


Fig. 2: Hysteresis Current control loop

III. DESIGN OF PFC BUCK HALF-BRIDGE CONVERTER BASED PMBLDCM DRIVE

The proposed PFC buck half-bridge converter is designed for a PMBLDCM drive with main considerations on PQ constraints at AC mains and allowable ripple in DC link voltage. The DC link voltage of the PFC converter is given as,

$$V_{dc} = 2 (N_2/N_1) V_{in} D \text{ and } N_2 = N_{21} = N_{22} \text{ --- (1)}$$

where N1, N21, N22 are number of turns in primary, secondary upper and lower windings of the high frequency (HF) isolation transformer, respectively.

Vin is the average output of the DBR for a given AC input voltage (Vs) related as,

$$V_{in} = 2\sqrt{2}V_s/\pi \text{ (2) -----}$$

A ripple filter is designed to reduce the ripples introduced in the output voltage due to high switching frequency for constant of the buck half-bridge converter. The inductance (Lo) of the ripple filter restricts the inductor peak to peak ripple current (ΔI_L) within specified value for the given switching frequency (fs), whereas, the capacitance (Cd) is calculated for a specified ripple in the output voltage (ΔV_C) [7-8]. The output filter inductor and capacitor are given as,

$$L_o = (0.5-D)V_{dc}/\{f_s(\Delta I_L)\} \text{ ----- (3)}$$

$$C_d = I_o/(2\omega\Delta V_C) \text{ ----- (4)}$$

The PFC converter is designed for a base DC link voltage of $V_{dc} = 400$ V at $V_{in} = 198$ V from $V_s = 220$ Vrms. The turns ratio of the high frequency transformer (N_2/N_1) is taken as 6:1 to maintain the desired DC link voltage at low input AC voltages typically at 170V. Other design data are $f_s = 40$ kHz, $I_o = 4$ A, $\Delta V_{Cd} = 4$ V (1% of V_{dc}), $\Delta I_{Lo} = 0.8$ A (20% of I_o). The design parameters are calculated as $L_o = 2.0$ mH, $C_d = 1600$ μ F.

H_a	H_b	H_c	E_a	E_b	E_c	S_1	S_2	S_3	S_4	S_5	S_6
0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	-1	+1	0	0	0	1	1	0
0	1	0	-1	+1	0	0	1	1	0	0	0
0	1	1	-1	0	+1	0	1	0	0	1	0
1	0	0	+1	0	-1	1	0	0	0	0	1
1	0	1	+1	-1	0	1	0	0	1	0	0
1	1	0	0	+1	-1	0	0	1	0	0	1
1	1	1	0	0	0	0	0	0	0	0	0

Table 1: Vsi Switching Sequence Based On The Hall Effect Sensor Signals

IV. MODELING OF THE PROPOSED PMBLDCM DRIVE

The main components of the proposed PMBLDCM drive are the PFC converter and PMBLDCM drive, which are modeled by mathematical equations and the complete drive is represented as a combination of these models.

A. PFC Converter

The modeling of the PFC converter consists of the modeling of a speed controller, a reference current generator and a PWM controller as given below.

1) Speed Controller

The speed controller, the prime component of this control scheme, is a proportional-integral (PI) controller which closely tracks the reference speed as an equivalent reference voltage. If at k th instant of time, $V^*_{dc}(k)$ is reference DC link voltage, $V_{dc}(k)$ is sensed DC link voltage then the voltage error $V_e(k)$ is calculated as,

$$V_e(k) = V^*_{dc}(k) - V_{dc}(k) \text{ ----- (5)}$$

The PI controller gives desired control signal after processing this voltage error. The output of the controller $I_c(k)$ at k th instant is given as,

$$I_c(k) = I_c(k-1) + K_p\{V_e(k) - V_e(k-1)\} + K_i V_e(k) \text{ --(6)}$$

where K_p and K_i are the proportional and integral gains of the PI controller.

2) Reference Current Generator

The reference input current of the PFC converter is denoted by i_{dc}^* and given as,

$$I^*_{dc} = I_c(k) u_{Vs} \text{ ----- (7)}$$

where u_{Vs} is the unit template of the voltage at input AC mains, calculated as,

$$u_{Vs} = v_d/V_{sm}; v_d = |v_s|; v_s = V_{sm} \sin \omega t \text{ ----- (8)}$$

where V_{sm} is the amplitude of the voltage and ω is frequency in rad/sec at AC mains.

3) PWM Controller

The reference input current of the buck half-bridge converter (i_{dc}^*) is compared with its sensed current (i_{dc}) to generate the current error $\Delta i_{dc} = (i_{dc}^* - i_{dc})$. This current error is amplified by gain k_{dc} and compared with fixed

frequency (f_s) saw-tooth carrier waveform $md(t)$ (as shown in Fig.2) in unipolar switching mode [7] to get the switching signals for the MOSFETs of the PFC buck half-bridge converter as,

$$\text{If } k_{dc} \Delta i_{dc} > md(t) \text{ then } S_A = 1 \text{ else } S_A = 0 \text{ --- (9)}$$

$$\text{If } -k_{dc} \Delta i_{dc} > md(t) \text{ then } S_B = 1 \text{ else } S_B = 0 \text{ --- (10)}$$

where S_A, S_B are upper and lower switches of the half-bridge converter as shown in Fig. 1 and their values '1' and '0' represent 'on' and 'off' position of the respective MOSFET of the PFC converter.

B. PMBLDCM Drive

The PMBLDCM drive consists of an electronic commutator, a VSI and a PMBLDC motor.

1) Electronic Commutator

The electronic commutator uses signals from Hall effect position sensors to generate the switching sequence for the voltage source inverter based on the logic given in Table I.

2) Voltage Source Inverter

Fig. 3 shows an equivalent circuit of a VSI fed PMBLDCM. The output of VSI to be fed to phase 'a' of the PMBLDC motor is given as,

$$v_{ao} = (V_{dc}/2) \text{ for } S_1 = 1 \text{ ----- (11)}$$

$$v_{ao} = (-V_{dc}/2) \text{ for } S_2 = 1 \text{ ----- (12)}$$

$$v_{ao} = 0 \text{ for } S_1 = 0, \text{ and } S_2 = 0 \text{ ----- (13)}$$

$$v_{an} = v_{ao} - v_{no} \text{ ----- (14)}$$

where v_{ao}, v_{bo}, v_{co} , and v_{no} are voltages of the three-phases and neutral point (n) with respect to virtual mid-point of the DC link voltage shown as 'o' in Fig. 3. The voltages v_{an}, v_{bn}, v_{cn} are voltages of three-phases with respect to neutral point (n) and V_{dc} is the DC link voltage. $S = 1$ and 0 represent 'on' and 'off' position of respective IGBTs of the VSI and considered in a similar way for other IGBTs of the VSI i.e. $S_3 - S_6$. Using similar logic $v_{bo}, v_{co}, v_{bn}, v_{cn}$ are generated for other two phases of the VSI feeding PMBLDC motor.

3) PMBLDC Motor

The PMBLDCM is represented in the form of a set of differential equations [3] given as,

$$v_{an} = R i_a + p \lambda_a + e_{an} \text{ ----- (15)}$$

$$v_{bn} = R i_b + p \lambda_b + e_{bn} \text{ ----- (16)}$$

$$v_{cn} = R i_c + p \lambda_c + e_{cn} \text{ ----- (17)}$$

where p is a differential operator (d/dt), i_a, i_b, i_c are three-phase currents, $\lambda_a, \lambda_b, \lambda_c$ are flux linkages and e_{an}, e_{bn}, e_{cn} are phase to neutral back emfs of PMBLDCM, in respective phases, R is resistance of motor windings/phase.

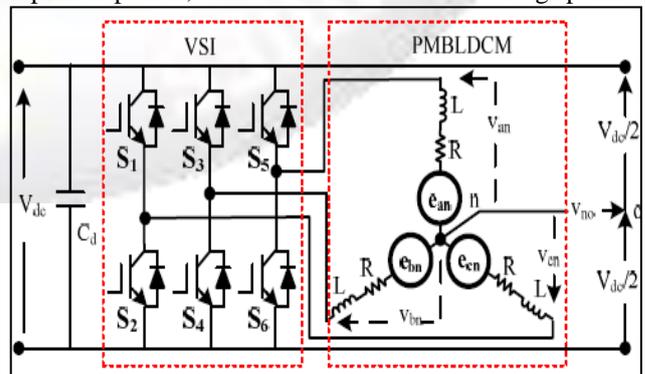


Fig. 3: Equivalent Circuit of a VSI fed PMBLDCM Drive The flux linkages are represented as,

$$\lambda_a = L i_a - M (i_b + i_c) \text{ ----- (18)}$$

$$\lambda_b = L_{ib} - M (i_a + i_c) \text{ ----- (19)}$$

$$\lambda_c = L_{ic} - M (i_b + i_a) \text{ ----- (20)}$$

where L is self-inductance/phase, M is mutual inductance of motor winding/phase. Since the PMBLDCM has no neutral connection, therefore,

$$i_a + i_b + i_c = 0 \text{ ----- (21)}$$

From Eqs. (14-21) the voltage between neutral terminal (n) and mid-point of the DC link (o) is given as,

$$v_{no} = \{v_{ao} + v_{bo} + v_{co} - (e_{an} + e_{bn} + e_{cn})\}/3 \text{ - (22)}$$

From Eqs. (18-21), the flux linkages are given as,

$$\lambda_a = (L+M) i_a, \lambda_b = (L+M) i_b, \lambda_c = (L+M) i_c, \text{ - (23)}$$

From Eqs. (15-17 and 23), the current derivatives in generalized state space form is given as,

$$p i_x = (v_{xn} - i_x R - e_{xn})/(L+M) \text{ ----- (24)}$$

where x represents phase a, b or c.

The developed electromagnetic torque T_e in the PMBLDCM is given as,

$$T_e = (e_{an} i_a + e_{bn} i_b + e_{cn} i_c) / \omega \text{ --- (25)}$$

where ω is motor speed in rad/sec,

The back emfs may be expressed as a function of rotor position (Θ) as,

$$e_x = K_b f_x(\Theta) \omega \text{ ----- (26)}$$

where x can be phase a, b or c and accordingly $f_x(\Theta)$ represents function of rotor position with a maximum value ± 1 , identical to trapezoidal induced emf given as,

$$f_a(\theta) = 1 \text{ for } 0 < \theta < 2\pi/3 \text{ ----- (27)}$$

$$f_a(\theta) = \{(6/\pi)(\pi - \theta)\} - 1 \text{ for } 2\pi/3 < \theta < \pi \text{ ----- (28)}$$

$$f_a(\theta) = -1 \text{ for } \pi < \theta < 5\pi/3 \text{ ----- (29)}$$

$$f_a(\theta) = \{(6/\pi)(\theta - 2\pi)\} + 1 \text{ for } 5\pi/3 < \theta < 2\pi \text{ ----- (30)}$$

The functions $f_b(\theta)$ and $f_c(\theta)$ are similar to $f_a(\theta)$ with a phase difference of 120° and 240° respectively.

Therefore, the electromagnetic torque is expressed as,

$$T_e = K_b \{f_a(\theta) i_a + f_b(\theta) i_b + f_c(\theta) i_c\} \text{ ----- (31)}$$

The mechanical equation of motion in speed derivative form is given as,

$$p\omega = (P/2) (T_e - T_L - B\omega) / (J) \text{ ----- (32)}$$

The derivative of the rotor position angle is given as,

$$p\Theta = \omega \text{ ----- (33)}$$

where P is no. poles, T_L is load torque in Nm, J is moment of inertia in kg-m² and B is friction coefficient in Nms/Rad. These equations (15-33) represent the dynamic model of the PMBLDC motor.

V. PERFORMANCE EVALUATION OF PROPOSED PFC DRIVE

The proposed PMBLDCM drive is modeled in Matlab-Simulink environment and evaluated for an air conditioning compressor load. The compressor load is considered as a constant torque load equal to rated torque with the speed control required by air conditioning system. A 1.5 kW rating PMBLDCM is used to drive the air conditioner compressor, speed of which is controlled effectively by controlling the DC link voltage. The detailed data of the motor and simulation parameters are given in Appendix.

The performance of the proposed PFC drive is evaluated on the basis of various parameters such as total harmonic distortion (THDi) power factor (PF) and efficiency of the drive system (η_{drive}) at different speeds of the motor as shown in Fig 4.



Fig. 4: Speed Variation

A. Performance under Speed Control

Figs. 5 show the performance of the proposed PMBLDCM drive under the speed control at constant rated torque (9.55 Nm) and 220 V AC mains supply voltage. Reference speed is changed from 900 rpm to 1500 rpm for the rated load performance of the compressor; from 900 rpm to 300 rpm for performance of the compressor at light load. It is observed that the speed control is fast and smooth in either direction i.e. acceleration or retardation with power factor maintained at nearly unity value. Moreover, the stator current of PMBLDCM is within the allowed limit (twice the rated current) due to the introduction of a rate limiter in the reference voltage.

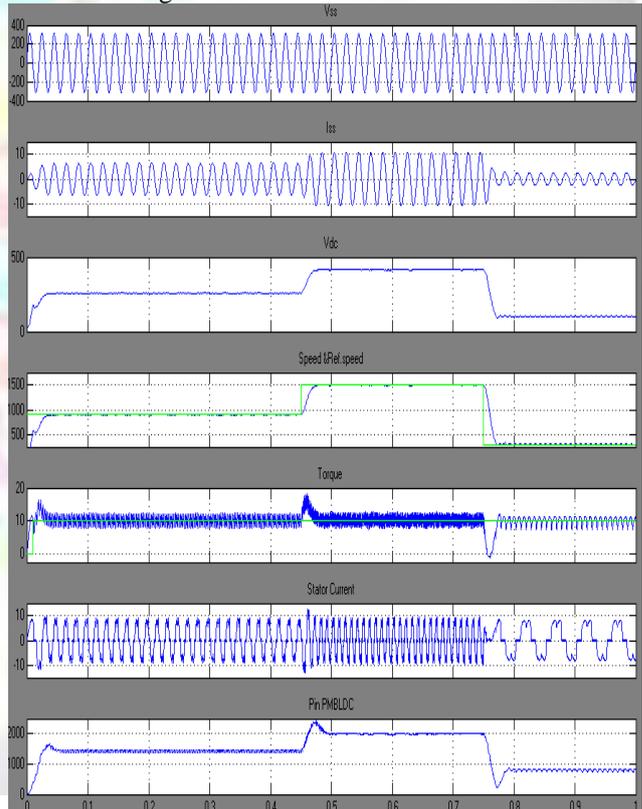


Fig. 5: Performance of the PMBLDCM under speed variation

B. Power Quality Performance

The performance of the proposed PMBLDCM drive in terms of various PQ parameters such as THDi, PF is summarized in Table-II and shown in Figs. 6 & Fig 7. Nearly unity power factor (PF) and reduced THD of AC mains current are observed in wide speed range of the

PMBLDCM as shown in Figs. 7a-b. The THD of AC mains current remains less than 5% along with nearly unity PF in wide range of speed as well

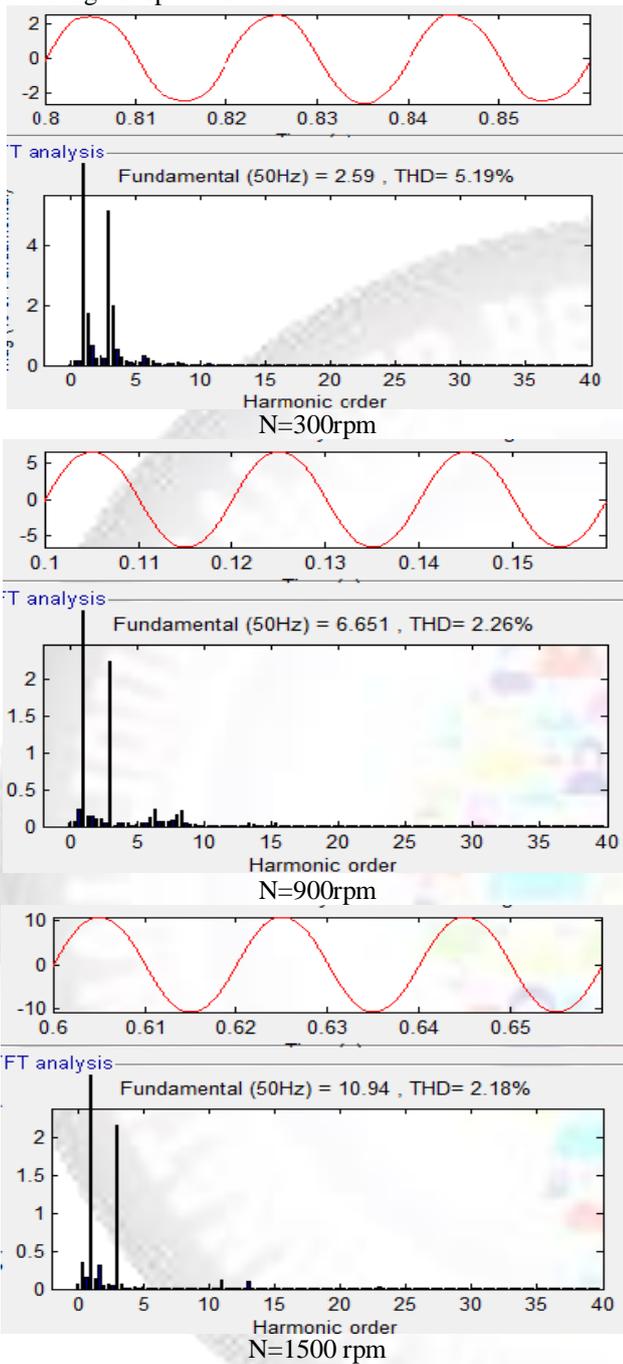


Fig. 6: (a) %THD of Source Current without HCC Controller under Various speeds

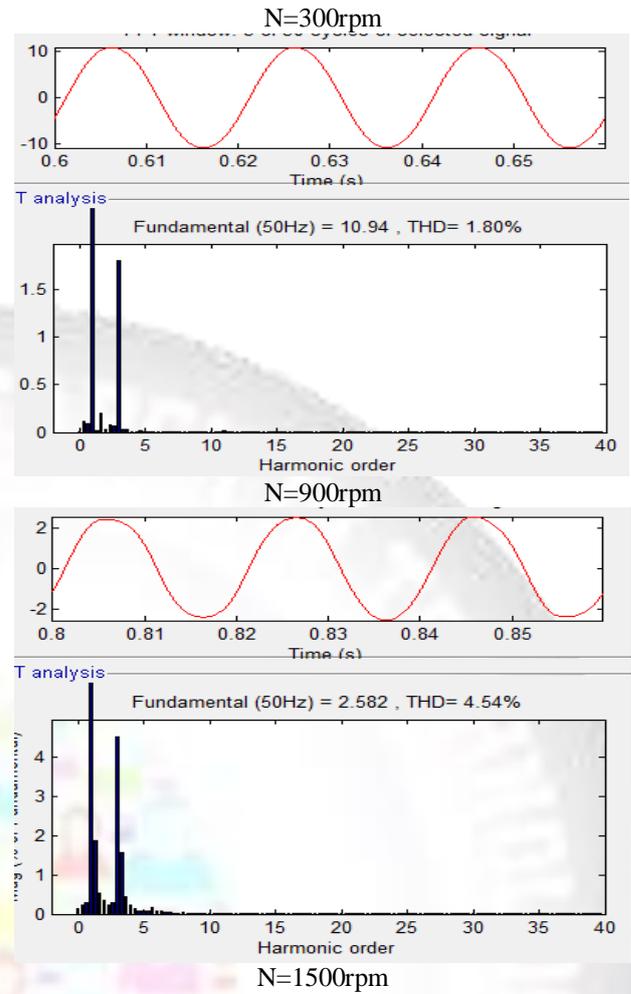
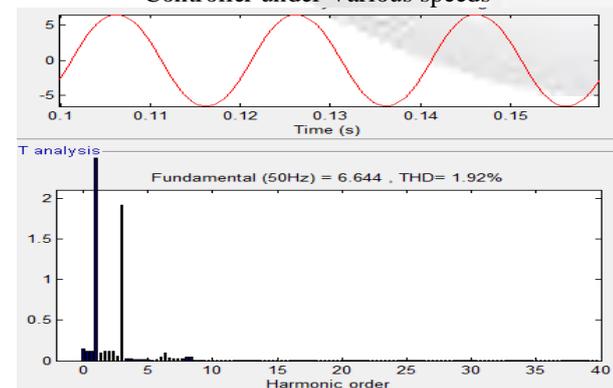


Fig. 6: (b) %THD of Source Current with HCC Controller under Various speeds

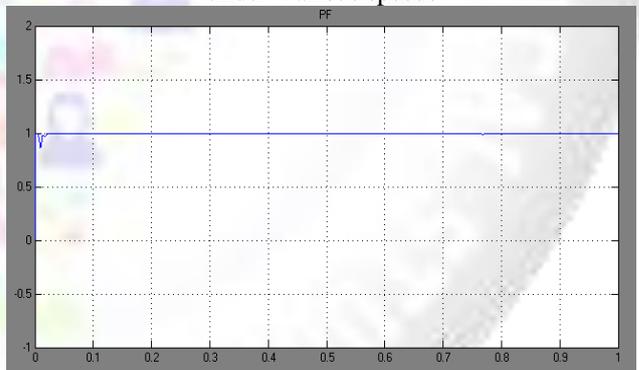


Fig. 7: Power Factor under speed variation

SPEED	%THD of I_s without HCC	%THD of I_s with HCC
300	5.19	4.54
900	2.26	1.92
1500	2.18	1.8

Table 2: %THD Comparison

VI. CONCLUSION

A new HCC based speed control strategy of a PMBLDCM drive is validated for a compressor load of an air conditioner which uses the reference speed as an equivalent reference voltage at DC link. The speed control is directly proportional to the voltage control at DC link. The

additional PFC feature to the proposed drive ensures nearly unity PF in wide range of speed. Moreover, power quality parameters of the proposed PMBLDCM drive are in conformity to an International standard IEC 61000-3-2 [5]. The proposed drive has demonstrated good speed control with energy efficient operation of the drive system in the wide range of speed. The proposed drive has been found as a promising candidate for a PMBLDCM driving Air-Con load in 1-2 kW power range.

APPENDIX

Rated Power: 1.5 kW, rated speed: 1500 rpm, rated current: 4.0 A, rated torque: 9.55 Nm, number of poles: 4, stator resistance (R): 2.8 Ω /ph., inductance (L+M): 5.21 mH/ph., back EMF constant (K_b): 0.615 Vsec/rad, inertia (J): 0.013 Kg-m². Source impedance (Z_s): 0.03 pu, switching frequency of PFC switch (f_s) = 40 kHz, capacitors (C1=C2): 15nF, PI speed controller gains (K_p): 0.145, (K_i): 1.45.

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